

Appl. No. 10/804,182  
Amdt. Dated April 5, 2004

**Amendments to the Drawings**

The attached drawing sheets include changes to Fig. 7, 8, 9a, 9b, 12a, 12b, 13a and 13b.

The sheet, which includes Fig. 7, replaces the original sheet including Fig. 7. In formal Figure 7, the incorrectly lined up time events have been corrected to reflect those shown in the originally filed informal drawings.

The sheet, which includes Fig. 8, replaces the original sheet including Fig. 8. In formal Figure 8, the incorrectly lined up time events have been corrected to reflect those shown in the originally filed informal drawings.

The sheet, which includes Fig. 9a, replaces the original sheet including Fig. 9a. In Figure 9a, the previously incorrect words "BLEG" and "PREDECODED Y ADDRESSES" have been corrected to read "BLEQ" and "PREDECODED X ADDRESSES". The lead lines indicating the "BIT LINE PAIR" have also been corrected.

The sheet, which includes Fig. 9b, replaces the original sheet including Fig. 9b. In Figure 9b, the previously omitted element 926 has been added and the words "BLT", "R5 AMP CLK" "DIFFERENT D-TYPE FLIP - FLO" and "DA OU" have been corrected to read "BL+", "RSAMPCLK", "DIFFERENTIAL D-TYPE FLIP-FLOP" and "DATA OUT".

The sheet, which includes Fig. 12a and 12b, replaces the original sheet including Fig. 12a and 12b. In formal Figures 12a and 12b, the incorrectly lined up time events have been corrected to

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reflect those shown in the originally filed informal drawings.

The sheet, which includes Fig. 13a, replaces the original sheet including Fig. 13a. In formal Figure 13a, the incorrectly lined up time events have been corrected to reflect those shown in the originally filed informal drawings.

The sheet, which includes Fig. 13b, replaces the original sheet including Fig. 13b. In formal Figure 13b, the incorrectly lined up time events have been corrected to reflect those shown in the originally filed informal drawings.